

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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In re Patent Application of:  
Eugene A. Fitzgerald

Application No. 10/774,890

Confirmation No. 8754

Filed: February 9, 2004

Art Unit: 2818

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Title: RELAXED SIGE PLATFORM FOR HIGH  
SPEED CMOS ELECTRONICS AND HIGH  
SPEED ANALOG CIRCUITS

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Examiner: D. A. Le

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form, based on Form PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Applicant submits herewith copies of cited non-patent literature in accordance with 37 CFR 1.98(a)(2).

This Information Disclosure Statement is filed after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Office Action or Notice of Allowance (37 CFR 1.97(c)).

Please charge our Deposit Account No. 07-1700 in the amount of \$180.00 covering the fee set forth in 37 CFR 1.17(p). The Director is hereby authorized to charge any deficiency in the filed fee, or credit any overpayment, to our Deposit Account No. 07-1700, under Order No. ASC-049C1.

Date: August 15, 2007

Respectfully submitted,

Electronic signature: /Matthew T. Currie/  
Matthew T. Currie  
Registration No. 58,533  
Agent for Applicant  
GOODWIN PROCTER LLP  
Exchange Place  
Boston, Massachusetts 02109  
(617) 570-1000

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